

Patent Application Serial No. 08/422,485 filed April 12, 1995 (now U.S. Patent No. 5,658,387); which is a continuation-in-part of copending U.S. Patent Application Serial No. 07/855,767 filed March 18, 1992 (now U.S. Patent No. 5,431,421); which is a continuation-in-part of U.S. Patent Application Serial No. 07/665,942 filed March 6, 1991 (now U.S. Patent No. 5,235,995); which is a continuation-in-part of U.S. Patent Application Serial No. 07/526,243 (now U.S. Patent 5,168,887).

Priority under 35 U.S.C. §120 is claimed based upon the above applications and U.S. Patent Application Serial Nos. 07/855,767 and 07/665,942 are also hereby incorporated by reference.

IN THE CLAIMS:

Please cancel claims 1-53 and please add the following new claims:

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54. (New) A wafer processor for processing a wafer such as a semiconductor wafer, a magnetic disk, or an optical disk, comprising:
- a processing bowl having an upper edge; and
 - a processor head comprising a motor and a wafer holder, the motor being carried above the wafer holder and operatively coupled to the wafer holder, the wafer holder being adapted to support a single wafer about a periphery of the wafer for rotation at a height below the upper edge of the processing bowl with the processor head extending outwardly of the periphery of the wafer.
55. (New) The wafer processor of claim 54 wherein the processor head is adapted to be lifted with respect to the processing bowl.
56. (New) The wafer processor of claim 54 wherein the wafer holder extends downwardly from an upper portion of the processor head to position a wafer below the upper portion of the processor head.
57. (New) The wafer processor of claim 56 wherein the upper portion of the processor head extends outwardly of the periphery of the wafer.